

HP-41C Matrix Input and Output Routine by John Kennedy
PPC Calculator Journal Volume 8 Number 3 Pg 9 May 1981
Modified version uses PPC ROM: QR/M1/M2/M3/M4/M5/BX/BE

```
01 LBL "MIO" ;Matrix Input and Output Routine
02 LBL A
03 "START REG. ?"
04 AVIEW
05 STOP
06 STO 07
07 "DIM: R?^C?"
08 AVIEW
09 STOP
10 STO 08
11 X<>Y
12 STO 09
13 SF 09
14 GTO 01
15 LBL B
16 CF 09
17 LBL 01
18 CF 29
19 RCL 07
20 STO 04
21 RCL 08
22 RCL 09
23 *
24 STO 03
25 LBL 02
26 RCL 04
27 XROM 20,35 ;M4 - Matrix, Register Address to (i,j)
28 FIX 0
29 " (" ;1 space, left parenthesis
30 ARCL Y ;Row number
31 ">"," ;Append comma
32 ARCL X ;Column number
33 ">")=" ;Append right parenthesis, equal sign
34 FC? 09
35 GTO 03
36 ">?" ;Append question mark
37 AVIEW
38 TONE 9
39 STOP
40 STO IND 04
41 GTO 04
42 LBL 03
43 FIX IND 05
44 ARCL IND 04
45 AVIEW
46 LBL 04
47 ISG 04
48 "" ;F0=null string (NOP)
49 DSE 03
50 GTO 02
51 BEEP
52 RTN
53 LBL C
54 XROM 20,36 ;M5 - Matrix, (i,j) to Register Address
55 " R" ;1 space, letter R (abbreviation for Register)
56 ARCL X
57 AVIEW
58 STO 04
59 E
60 STO 03
61 CF 09
62 GTO 02
63 END
```

HP-41C Matrix Input and Output Routine by John Kennedy
PPC Calculator Journal Volume 8 Number 3 Pg 9 May 1981
Modified version uses PPC ROM: QR/M1/M2/M3/M4/M5/BX/BE

0081C000F4004D494FCF66FC5354415254205245472E203F7E
8437FA44494D3A20523F5E433F7E84387139A809B200CF67A9
0902A91D2734282942330324A5239C00F220289B72F27F2C9B
73F37F293DAD09B400F27F3F7E9F09849184B500049C859B84
7E059604F09703B3008685CF68A524F220529B737E341B33A9
09B300C0000D84

129 BYTES

HP-41C Matrix Input/Output Routine by John Kennedy PPC V8 N3 P9 May 1981

Program Registers Needed: 19

Row 1 (1 - 3)



Row 2 (3 - 7)



Row 3 (7 - 10)



Row 4 (11 - 18)



Row 5 (19 - 29)



Row 6 (29 - 33)



Row 7 (34 - 40)



Row 8 (41 - 49)



Row 9 (49 - 56)



Row 10 (56 - 63)

